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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10043516	FILING DATE 01/11/2002	CLASS 361	SUBCLASS	GAU 2836	EXAMINER HU
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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

GERMANY 10101330.2 01/13/2001

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO DE 01007
TITLE : Electric or electronic circuit arrangement and method of protecting said circuit arrangement from manipulation and/or abuse		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		
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